

Electronic Acknowledgement Receipt

EFS ID:	4065889
Application Number:	10807648
International Application Number:	
Confirmation Number:	3901
Title of Invention:	Cached memory system and cache controller for embedded digital signal processor
First Named Inventor/Applicant Name:	Gilbert Christopher Sih
Customer Number:	23696
Filer:	Joseph B. Agusta/Theresa Boyce
Filer Authorized By:	Joseph B. Agusta
Attorney Docket Number:	030459
Receipt Date:	06-OCT-2008
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Appeal Brief Filed	030459_AppealBrief_10-06-08.pdf	226507 4e5714082711028dc55ee6c00db08d0c5038f3c5	no	25

Warnings:

Information:

2	Fee Worksheet (PTO-06)	fee-info.pdf	29952 479aa94040b30e41afd5ed8a2c883f3c986cb099	no	2
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Warnings:

Information:

Total Files Size (in bytes):			256459
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